Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
Ĺ1	2	"6546451"	US-PGPUB; USPAT	OR	ON	2005/02/02 10:28
L2	1	"6374307".pn.	US-PGPUB; USPAT	OR	ON	2005/02/02 10:29
S1	7	asynchronous adj crossbar	US-PGPUB; USPAT	OR	ON	2005/02/02 09:40
S2	144444	(system adj on adj chip) or SOC	US-PGPUB; USPAT	OR	ON	2004/08/15 09:43
S3	3	((system adj on adj chip) or SOC) and (synchronous adj module)	US-PGPUB; USPAT	OR	ON	2004/08/19 18:43
S4	0	(synchronous adj module) and (clock adj converter)	US-PGPUB; USPAT	OR .	ON	2004/08/15 08:54
S5	.,,3	(synchronous adj module) and (clock adj domain)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:30
S6	4	("5845107" "6018782" "6035364" "6128688").PN.	USPAT	OR	ON	2004/08/15 09:03
S 7	1	(plurality adj synchronous adj module) and (clock adj domain)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:30
S8	0	(plurality adj synchronous) and (on adj chip adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:31
S9	6	(plurality adj synchronous) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:35
S10	1	(plurality adj synchronous adj device) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:37
S11	636	asynchronous adj bus	US-PGPUB; USPAT	OR	ON	2004/08/15 09:35
S12	5	((system adj on adj chip) or SOC) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:35
S13	9	(synchronous adj device) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:52
S14	27	(router) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:39
S15	0	system adj on adj chip	US-PGPUB; USPAT	OR	ON	2004/08/15 09:43
S16	4103	system adj chip	US-PGPUB; USPAT	OR	ON	2004/08/15 09:54
S17	- 30	(asynchronous adj bus) and (system adj chip)	US-PGPUB; USPAT	OR .	ON	2004/08/15 09:43
S18	0	(plurality adj synchronous adj device) near3 (chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:58
S19	0	(plurality adj synchronous adj device) near3 (bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:53

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S20	0	(plurality adj synchronous adj module) near3 (bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:53
S21	0	(plurality adj synchronous adj module) same (bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 09:53
S22	138	(system adj chip) with interconnect\$5	US-PGPUB; USPAT	OR	ON	2004/08/15 09:54
S23	4	plurality adj synchronous adj device	US-PGPUB; USPAT	OR	ON	2004/08/15 10:02
S24	8	("4652993" "5469549" "5504675" "5515523" "5524098" "5586299" "6067595" "6078527").PN.	USPAT	OR	ON	2004/08/15 10:01
S25	1	(plurality adj synchronous) and (clock adj rate) and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:04
S26	253	"710"/\$.ccls. and (asynchronous adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:04
S27	0	"710"/\$.ccls. and (asynchronous adj bus) and (synchronous adj module)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:05
S28	6	"710"/\$.ccls. and (asynchronous adj bus) and (synchronous adj device)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:12
S29	1	"6353867".pn.	US-PGPUB; USPAT	OR	ON	2004/08/15 10:12
S30	1256	710/305.ccls.	US-PGPUB; USPAT	OR	ON	2004/08/15 10:13
S31	27	710/305.ccls. and (system adj chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:19
S32	0	710/305.ccls. and (asynchronous adj splitter)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:20
S33	21	710/305.ccls. and (asynchronous	ÜS-PGPUB;	OR 🦠	ON -	2004/08/15 11:02
		adj bus)	USPAI		ł	
S34	0	710/305.ccls. and (on adj chip adj interconnect\$5)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:21
S35	14	710/305.ccls. and (chip adj interconnect\$5)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:25
S36	102	"710"/\$.ccls. and (chip adj interconnect\$5)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:36
S37	. 66	"710"/\$.ccls. and (cross\$5 adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:54
S38	0	"10136025".pn.	US-PGPUB; USPAT	OR	ON	2004/08/15 10:38
S39	32	crossbar adj bus\$4	US-PGPUB; USPAT	OR	ON	2004/08/15 10:47
S40	0	(crossbar adj bus\$4) and ((system adj on adj chip) or SOC)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:48

S41	286	(cross\$5 adj bus)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:48
S42	2	(cross\$5 adj bus) and (system adj chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 10:48
S43	1	"6185629".pn.	US-PGPUB; USPAT	OR	ON	2004/08/15 10:59
S44	1	"5471592".pnpn.	US-PGPUB; USPAT	OR	ON	2004/08/15 10:59
S45	0	710/305.ccls. and (asynchronous adj crossbar)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:02
S46	0	"710"/\$.ccls. and (asynchronous adj crossbar)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:03
S47	3	"370"/\$.ccls. and (asynchronous adj crossbar)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:03
S48	683	"370"/\$.ccls. and (crossbar adj switch)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:03
S49	317	"710"/\$.ccls. and (crossbar adj switch)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:03
S50	3	"710"/\$.ccls. and (crossbar adj	US-PGPUB;	OR	ON	2004/08/15 11:04
		switch) and (synchronous adj device)	USPAT		1 <u>3</u>	
S51	6	"710"/\$.ccls. and (crossbar adj switch) and (system adj chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:17
S52	127	(crossbar adj switch) and (system adj chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:17
S53	0	(crossbar adj switch) and (system adj chip) and (synchronous adj device)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:18
S54	127	(crossbar adj switch) and (system adj chip)	US-PGPUB; USPAT	OR	ON	2004/08/15 11:18
S55	62	asynchronous with crossbar	US-PGPUB; USPAT	OR	ON	2004/08/19 13:49
S56		"5838684".pn.	US-PGPUB; USPAT	OR	ON	2004/08/19 13:49
S57	0	710/317.ccls. and ((system adj on adj chip) or SOC) and (synchronous adj module)	US-PGPUB; USPAT	OR	ON	2004/08/19 18:44
S58	Ö	710/317.ccls. and (synchronous adj module)	US-PGPUB; USPAT	OR :	ON	2004/08/19 18:44
S59	191	710/317.ccls.	US-PGPUB; USPAT	OR	ON	2004/08/19 18:44
S60	44	710/317.ccls. and asynchronous	US-PGPUB; USPAT	OR	ON	2004/08/19 18:44
S61	191	710/317.ccls.	US-PGPUB; USPAT	OR	ON	2004/09/09 16:25

S62	0	710/317.ccls. and (clock adj converter)	US-PGPUB; USPAT	OR	ON	2004/09/09 16:25
S63	104	710/317.ccls. and (clocks)	US-PGPUB; USPAT	OR	ON	2004/09/09 16:25
S64	0	710/317.ccls. and (asynchronous adj crossbar)	US-PGPUB; USPAT	OR	ON	2004/09/09 16:26
S65	44	710/317.ccls. and (asynchronous)	US-PGPUB; USPAT	OR	ON	2004/09/10 10:20
S66	29	710/317.ccls. and (asynchronous) and synchronous	US-PGPUB; USPAT	OR	ON	2004/09/09 16:26
S67	0	(clock adj converter) and crossbar	US-PGPUB; USPAT	OR	ON	2004/09/10 10:05
S68	190	clock adj converter	US-PGPUB; USPAT	OR	ON	2004/09/10 10:05
S69	41	(clock adj converter) and asynchronous	US-PGPUB; USPAT	OR	ON	2004/09/10 10:10
S70	37	(clock adj converter) and (data adj transfer)	US-PGPUB; USPAT	OR	ON	2004/09/10 10:11
S71	0	710/317.ccls. and (clock adj converters)	US-PGPUB; USPAT	OR	ON	2004/09/10 10:23
S72	6	"710"/\$.ccls. and (clock adj converters)	US-PGPUB; USPAT	OR	ON	2004/09/10 10:21
\$73	1	710/317.ccls. and (plurality adj clocks)	US-PGPUB; USPAT	OR	ON	2004/09/10 10:25
S74	20	710/317.ccls. and (FIFO same crossbar)	US-PGPUB; USPAT	OR	ON	2004/09/10 11:10
S75	5	"710"/\$.ccls. and (synchronous with (logic adj device))	US-PGPUB; USPAT	OR	ON	2004/09/10 11:17
S76	25	"710"/\$.ccls. and (synchronous same (logic adj device))	US-PGPUB; USPAT	OR	ON	2004/09/10 11:17
S77	808	data adj token	US-PGPUB; USPAT	OR	ON	2004/09/13 10:48
S78	2	(data adj token) same crossbar	US-PGPUB; USPAT	OR	ON	2004/09/13 11:26
S79	1	"6715023".pn.	US-PGPUB; USPAT	OR	ON	2004/09/13 17:13
S80	3	seitz adj arbiter	US-PGPUB; USPAT	OR	ON	2004/09/13 17:37
S81	26	crossbar same sonet	US-PGPUB; USPAT	OR	ON	2004/09/13 17:47
S82	0	crossbar same (sonet adj interface)	US-PGPUB; USPAT	OR	ON	2004/09/13 17:47
S83	6	(crossbar adj switch) and (sonet adj interface)	US-PGPUB; USPAT	OR	ON	2004/09/13 17:49

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S84	13	(crossbar) and (sonet adj interface)	US-PGPUB; USPAT	OR	ON	2004/09/13 18:16
S85	2916	netlist	US-PGPUB; USPAT	OR	ON	2004/09/13 18:16
S86	147	netlist and crossbar	US-PGPUB; USPAT	OR	ON	2004/09/14 09:02
S87	1	"6715023".pn.	US-PGPUB; USPAT	OR	ON	2004/09/13 18:19
S88	14	netlist same crossbar	US-PGPUB; USPAT	OR	ON	2004/09/14 10:28
S89	1	"6715023".pn.	US-PGPUB; USPAT	OR	ON	2004/09/14 10:28
S90	1	"6715023".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:15
S91	i	"6002861".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:16
S92	, 1	"5838684".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:17
S93	1	"6546451".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:17
S94	1	"6101565".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:19
S95	1	"5517662".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:19
S96	13	"710"/\$.ccls. and (asynchronous\$5 with crossbar)	US-PGPUB; USPAT	OR	ON	2005/01/25 11:27
S97	1	"5517662".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:28
S98	1	"6546451".pn.	US-PGPUB; USPAT	OR	ON	2005/01/25 11:28
S99	ì	"6038656".pn.	US-PGPUB; USPAT	OR	ON	2005/01/26 14:42
S10 0	64	asynchronous with crossbar	US-PGPUB; USPAT	OR	ON	2005/01/26 15:04
S10 1	1	."6112016".pn	US-PGPUB; USPAT	OR	ON	2005/01/27 16:59
S10 2	71	(data adj token) and crossbar	US-PGPUB; USPAT	OR	ON	2005/01/27 17:05
S10 3	2	(data adj token) same crossbar	US-PGPUB; USPAT	OR	ON	2005/01/27 17:05
S10 4	0	710/244.ccls. and (data adj token) and crossbar	US-PGPUB; USPAT	OR	ON	2005/01/28 08:49
S10 5	1	710/244.ccls. and (token) and crossbar	US-PGPUB; USPAT	OR	ON	2005/01/28 09:48

S10 6	1	"6546451".pn.	US-PGPUB; USPAT	OR	ON	2005/01/28 14:17
S10 7	0	"710"/\$.ccls. and (seitz adj arbiter)	US-PGPUB; USPAT	OR	ON	2005/01/28 14:43
S10 8	2366	"710"/\$.ccls. and (arbiter)	US-PGPUB; USPAT	OR.	ON	2005/01/28 14:17
S10 9	2	"370"/\$.ccls. and (Seitz adj arbiter)	US-PGPUB; USPAT	OR	ON	2005/01/28 14:24
S11 0	3	(Seitz adj arbiter)	US-PGPUB; USPAT	OR	ON	2005/01/28 14:33
S11 1	1	"5517662".pn.	US-PGPUB; USPAT	OR	ON	2005/01/28 14:40
S11 2	0	buid adj self adj test	US-PGPUB; USPAT	OR	ON	2005/01/28 14:41
S11 3	10	build adj self adj test	US-PGPUB; USPAT	OR	ON	2005/01/28 14:42
S11 4	0	S113 and crossbar	US-PGPUB; USPAT	OR	ON	2005/01/28 14:42
S11 5	0	S113 and router	US-PGPUB; USPAT	OR	ON	2005/01/28 14:42
S11 6	2	S113 and switch	US-PGPUB; USPAT	OR	ON	2005/01/28 14:42
S11 7	0	"710"/316.ccls. and (seitz adj arbiter)	US-PGPUB; USPAT	OR	ÓN	2005/01/28 14:43
S11 8	3	"710"/316.ccls. and (BIST)	US-PGPUB; USPAT	OR	ON	2005/01/28 15:12
S11 9	2	"710"/317.ccls. and (BIST)	US-PGPUB; USPAT	OR	ON	2005/01/28 14:55
S12 0	1	"6763418".pn.	US-PGPUB; USPAT	OR	ON	2005/01/28 14:55
S12 1	6	"710"/316.ccls. and (CPU same CPU same crossbar)	US-PGPUB; USPAT	OR	ON	2005/01/28 15:28
S12 2	4	"710"/316.ccls. and (multiprocessor same crossbar)	US-PGPUB; USPAT	OR	ON	2005/01/28 15:29
S12 3	65	asynchronous with crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 10:10
S12 4	1	S123 same (data adj token)	US-PGPUB; USPAT	OR	ON	2005/01/31 17:53
S12 5	2	S123 and (data adj token)	US-PGPUB; USPAT	OR	ON	2005/01/31 17:53
S12 6	6	S123 and repeater	US-PGPUB; USPAT	OR	ON	2005/01/31 18:04
S12 7	1	S123 same repeater	US-PGPUB; USPAT	OR	ON	2005/01/31 18:05

S12 8	1	S123 and (Seitz adj arbiter)	US-PGPUB; USPAT	OR	ON	2005/01/31 18:18
S12 9	1	S123 and (Seitz)	US-PGPUB; USPAT	OR	ON	2005/01/31 18:06
S13 0	3	(Seitz adj arbiter)	US-PGPUB; USPAT	OR	ON	2005/02/01 10:09
S13 1	1	"6546451".pn.	US-PGPUB; USPAT	OR	ON	2005/02/01 10:09
S13 2	65	asynchronous with crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 11:56
S13 3	24	S132 and priority and congestion	US-PGPUB; USPAT	OR	ON	2005/02/01 11:05
S13 4	1	"6763418".pn.	US-PGPUB; USPAT	OR	ON	2005/02/01 11:05
S13 5	6	S132 and SONET	US-PGPUB; USPAT	OR	ON	2005/02/01 11:53
S13 6	1	"6002861".pn.	US-PGPUB; USPAT	OR	ON _.	2005/02/01 12:47
S13 7	183	motherboard same CMOS	US-PGPUB; USPAT	OR	ON	2005/02/01 12:47
S13 8	0	motherboard same CMOS same crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 12:49
S13 9	,,2	S137 and crossbar	US-PGPUB; USPAT	OR	ON.	2005/02/01 12:47
S14 0	0	CMOS adj motherboard	US-PGPUB; USPAT	OR	ON	2005/02/01 12:49
S14 1	78	CMOS with motherboard	US-PGPUB; USPAT	OR	ON	2005/02/01 12:50
S14 2	0	S141 and crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 12:49
S14 3	1310	CMOS adj IC	US-PGPUB; USPAT	OR	ON	2005/02/01 12:51
S14 4	0	S143 same crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 12:50
S14 5	2	S143 and crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 12:50
S14 6	5228	CMOS adj Integrated adj Circuit	US-PGPUB; USPAT	OR	ON	2005/02/01 12:52
S14 7	48	S146 and crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 13:41
S14 8	1	"20040081169"	US-PGPUB; USPAT	OR	ON	2005/02/01 13:36
S14 9	224	SONET and crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 13:41

S15 0	31	SONET same crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 13:45
S15 1	1	"5715248".pn.	US-PGPUB; USPAT	OR	ON	2005/02/01 13:44
S15 2	0	(SONET adj interface) same crossbar	US-PGPUB; USPAT	OR	ON	2005/02/01 13:45